

INTEGRATED CIRCUIT WITH BI-MODAL DATA STROBE

ABSTRACT OF THE DISCLOSURE

A memory device that has two operating modes. In the first mode the data strobe is source synchronous and is driven by the memory device when data is being transmitted. In the second mode the data strobe is not driven by the memory device. In this mode the data strobe signal is used as a free running clock to sample write data. The capture of read data by the controller is timed by the controller with a calibrated internal timing reference from the system clock.